

Enhancing the Self-Heating Capability of AlGa_N/Ga_N HEMT Gadgets with Fourth-Generation Semiconductor Components

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Abstract. GaN possesses numerous exceptional properties, such as wide bandgap and high thermal conductivity, which make it an ideal material for fabricating semiconductor devices. The high electric field and current in the channel, coupled with the high power density, make it inevitable that HEMT devices will generate a great deal of heat. Thus, an increase in temperature will unavoidably cause the DC and microwave characteristics of the device to deteriorate. At present, the fourth-generation semiconductor materials with new ultra-broad or ultra-narrow band gaps, mainly Ga₂O₃, GaSb, diamond and AlN, have emerged. In conventional situations, the substrate material will be Si, Sapphire, or the more advanced third-generation material SiC compared to the former, and the passivation layer will use SiN as the material. Our research is in progress to replace the traditional SiN and third-generation SiC with diamond and GaSb.

Keywords. GaN HEMT self-heating fourth-generation semiconductor

1. Introduction

Due to the perpetual advancement of modern technology, the semiconductor industry has become more and more irreplaceable as human society has entered the era of communication networks. In all aspects of our lives, semiconductors are indispensable^[1]. The industry fields based on GaN materials, such as future smart grids, cloud computing and cloud native, industrial robots, as well as emerging industries such as HEMT and MESFET, have very broad prospects^[2]. GaN has a bandgap of 3.39 eV, making it an ideal choice for producing high-power and devices^[3]. Since most devices currently operate in higher temperature environments, conventional packaging and heat dissipation technology can no longer meet the needs of many high-power, high current, and high voltage working environments today. Therefore, the GaN HEMT device needs to be improved from its internal structure. In this article, we have decided to transform three aspects: substrate, passivation layer, and top layer^[4]. We will use diamonds as the top and bottom layers. Research has revealed that epitaxial growth of the solid solution material, with GaSb as the substrate, can effectively reduce the stress, defects, and other issues caused by lattice misalignment^[5].

By utilizing Silvaco TCAD software, this article has stimulated the GaN HEMT device, examining its self-heating effect, current collapse, current intensity, and transfer

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characteristics.

2. Device Structures and Models

GaN HEMT devices are simulated with Silvaco TCAD software, and Figure 1 displays conventional HEMT devices^[6]. The base material is a 2.45 μm SiC layer, and it has a 50 nm AlN nucleation layer on the substrate, which serves to buffer lattice misalignment and thermal stress between the substrate and buffer, thereby creating a good separation and buffering effect^[7-11]. Located above the 2.26 μm GaN buffer layer, the source and drain electrodes, measuring 0.6 μm and 0.352 μm respectively, are arranged in a sequence of layers that include a 22 nm AlGaN barrier layer, and a 4 nm GaN cap layer structure. The top layer is the SiN passivation layer^[26-30]. The Gummel and Newton combination iterative method is employed for this iterative method^[12-25]. Figure 2 displays the device designed by us for the application of fourth-generation semiconductor materials. Diamond is implemented as the substrate material instead of SiC^[31]. In addition, GaSb is the passivation layer and diamond is the heat dissipation layer. All other parameters are the same as conventional materials.

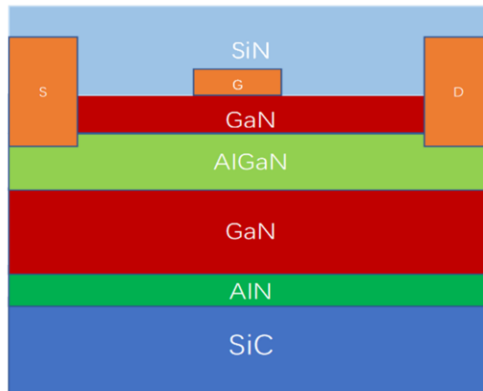


Figure 1. Traditional device structure



Figure 2. Novel device structure

3. Simulation Results and Discussion

3.1 Heat distribution

Figure 3 and Figure 4 respectively show the temperature distribution of traditional and new devices. From the pictures, we can see that under a gate voltage of 0 V, the heat distribution range of conventional devices is larger and the distribution area is wider. The portion of the device with a temperature of 400 K makes up approximately 13% of the total area^[32]. Near the gate electrode, the temperature can reach a maximum of 456 K, while the average temperature near the channel is 420 K, thus diminishing and intensifying lattice scattering, thus diminishing carrier mobility of the channel. In Figure 4, the area where the maximum temperature is located is only 363 K, not exceeding 400 K. Compared to traditional device junctions, the average temperature in the channel region decreases by 24% to only 320 K. Therefore, the new structure has more advantages in heat control^[33].

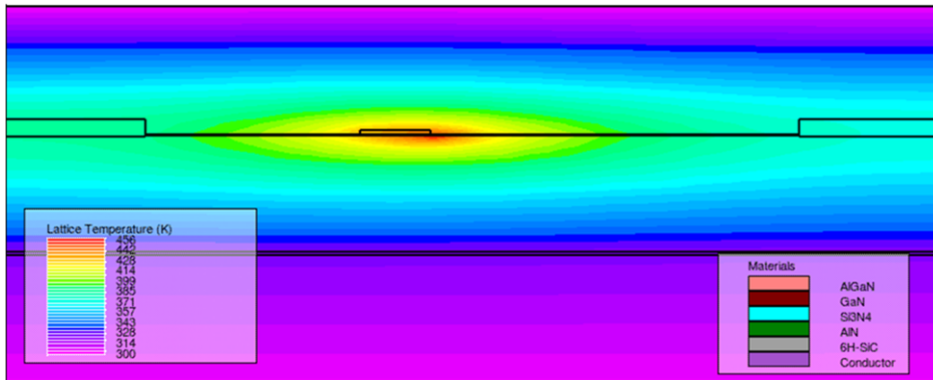


Figure 3. Temperature distribution of traditional device structure.

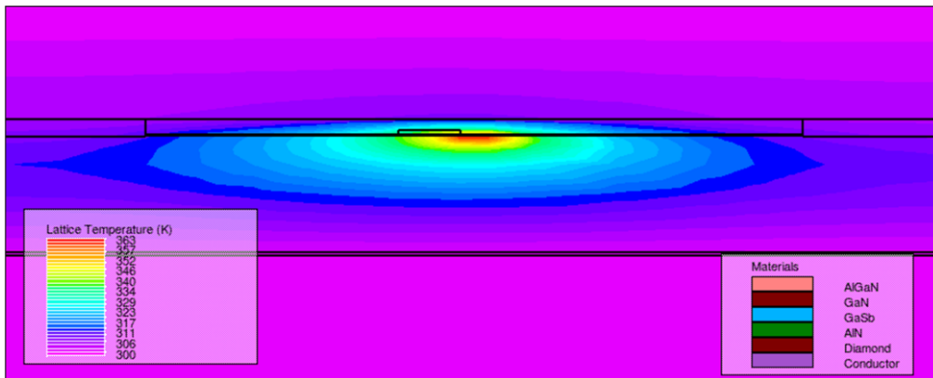


Figure 4. Temperature distribution of Novel device structure.

3.2 Current collapse and peak current

Figure 5 displays the output curves of both conventional and novel structures. The new structure's peak current can reach 1.907 A/mm^{-1} , while the conventional structure's peak

current is 1.315 A/mm^{-1} , which is a 45% increase at a gate voltage of 0 V ^[34]. In the end, the immediate crash rate of the traditional structure is 20%, while the new structure is 3%, reducing it by 17% and achieving significant results.

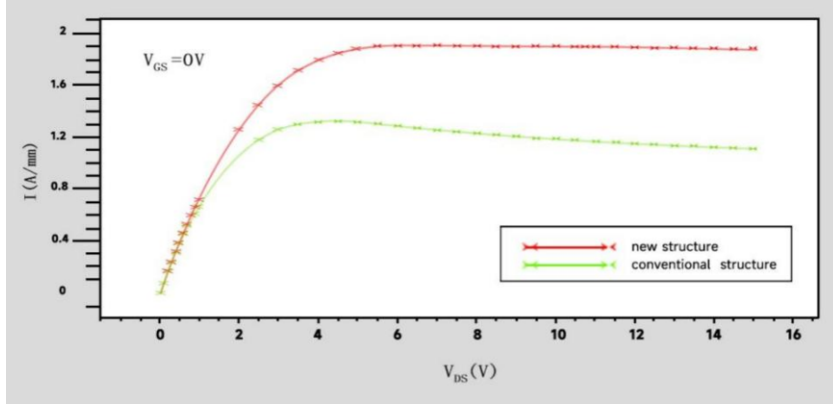


Figure 5. Output curves of traditional device structure and novel device structure.

3.3 Transfer characteristics of devices

The transfer characteristic curves for both the traditional and innovative structures are depicted in Figure 6. Upon examining the figure, it can be seen that the curve of the new structures is closer to the left. As temperature increases, this characteristic becomes more obvious^[35]. The output current of the traditional structure drain is smaller than that of the new structure at the identical gate voltage, and it can be deduced that the new device has a more potent gate control capacity through the assessment of its transfer characteristics^[36].

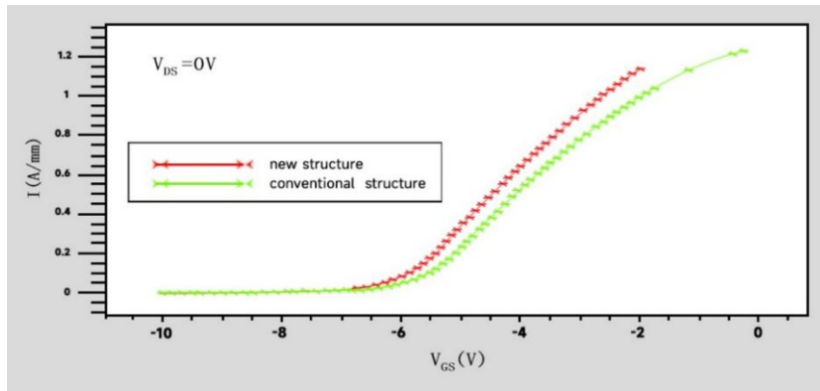


Figure 6. Transfer characteristic curves of traditional device structure and novel device structure.

4. Conclusion

The research into GaN HEMT devices has made tremendous strides, yet their self-heating capability causes a rise in channel temperature, thus making the thermal

reliability issue more severe. This study focuses on the application of HEMT devices in high-power areas^[37]. We replaced SiC substrate layer and SiN passivation layer with the fourth-generation semiconductor materials diamond and GaSb, respectively. Adding a diamond heat dissipation layer to the top of the device enhances its heat dissipation ability. The mean temperature declined by 24%, the maximum temperature slipped by 93 K, and the peak current slipped by 45%, due to the difference between traditional structures. Furthermore, the rate of current collapse improvement can reach 17% as well. As a result, the new structure proposed in this study improves the thermal and electrical properties of GaN HEMT^[38]. Despite the fact that the fourth generation of semiconductors has yet to be extensively employed in many areas, and further evidence is needed to validate it, it is still in the realm of the third generation^[39]. Nevertheless, the utilization of the fourth generation of semiconductors in HEMT devices has still been successful^[40].

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